## ABSTRACT OF THE DISCLOSURE

There is provided a semiconductor device which is capable of solving a

problem of threshold control in CMOS transistor, accompanied with combination
of a gate insulating film having a high dielectric constant and a metal gate
electrode, and significantly enhancing performances without deterioration in
reliability of a device. The semiconductor device includes a gate insulating film
composed of a material having a high dielectric constant, and a gate electrode.

A portion of the gate electrode making contact with the gate insulating film has a